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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re application of : NICHOLAS J. RICHARDSON

U.S. Serial No. : 09/909,121

Filed : July 19, 2001

For : PROGRESSIVE INSTRUCTION FOLDING IN A PROCESSOR
WITH FAST INSTRUCTION DECODE

Group No. : (Not yet known)

Examiner : (Not Yet Assigned)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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MAY 29 2003
Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement.

This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO/SB/08A. These references were located in a European Search Report issued in the related European Patent Application No. EP 02 25 4953, a copy of which is attached hereto. Copies of the listed references are submitted herewith.

U.S. Patents

Patent No.	Inventor	Date
5,504,932	Vassiliadis, et al.	April 2, 1996

6,237,086

Koppala, et al.

May 22, 2001

Foreign Patents

Patent No.	Country	Date
WO 97/13193	PCT	April 10, 1997

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

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Date: May 20, 2003

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